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W&B-INF-952

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Date:

September 30, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.

: 10/034,070

Confirmation No. 9447

Applicant

: Robert Kaiser et al.

Filed

: December 20, 2001

TC/A.U.

: 2133

Examiner

: John P. Trimmings

Title:

: Integrated Circuit Having a Data Processing

Unit and a Buffer Memory

Docket No.

: W&B-INF-952

Customer No. :

24131

Hon. Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office action dated August 9, 2005, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.